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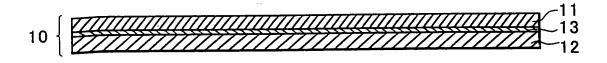


FIG.2

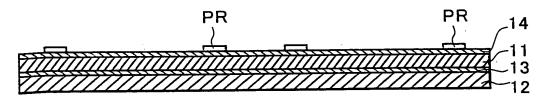


FIG.3

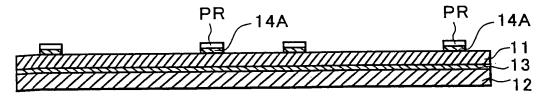




FIG.4

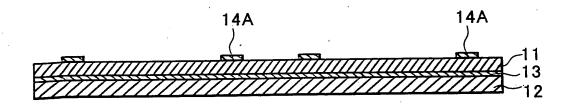


FIG.5

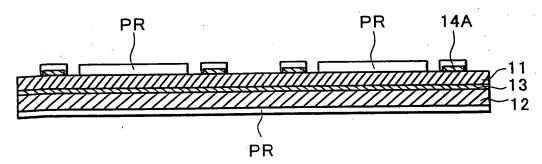
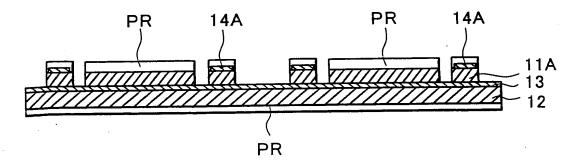


FIG.6



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## FIG.7

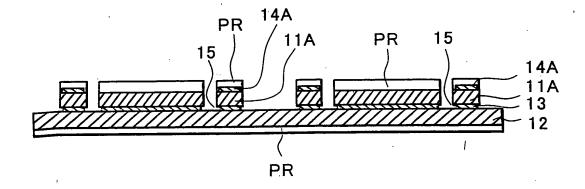
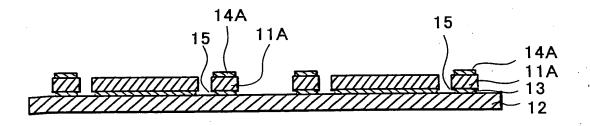
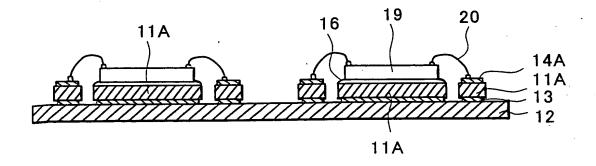


FIG.8



# FIG.9



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# FIG.10

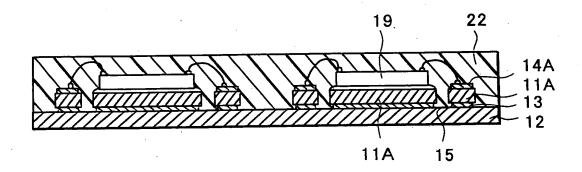


FIG.11

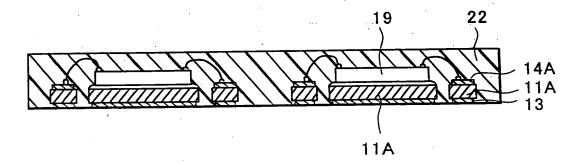
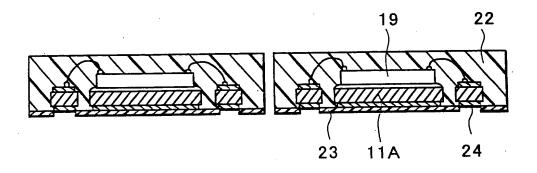


FIG.12



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Matter No.: 14225-028001
Applicant(s): Hideki Mizuhara et al.
METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.13

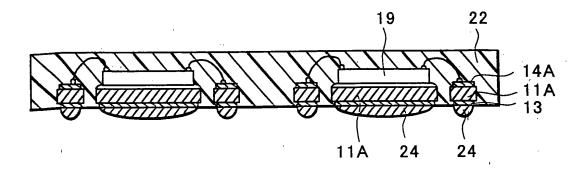


FIG.14

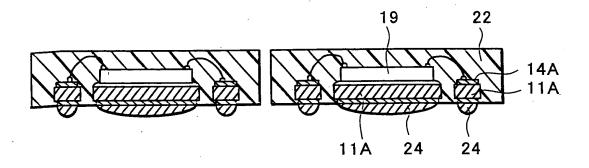


FIG. 15

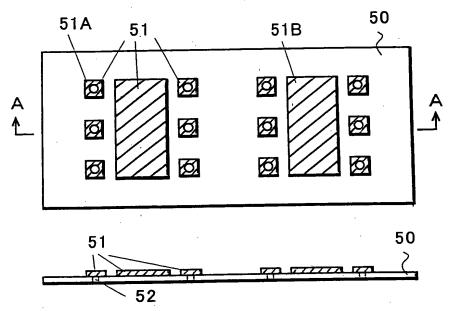


FIG.16

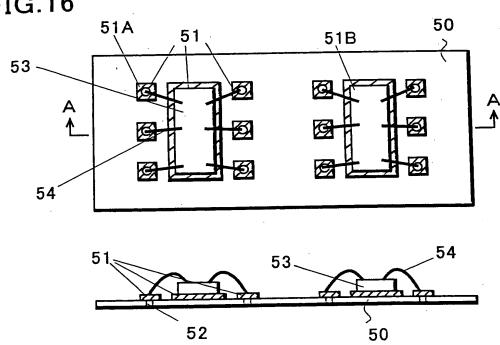


FIG.17A

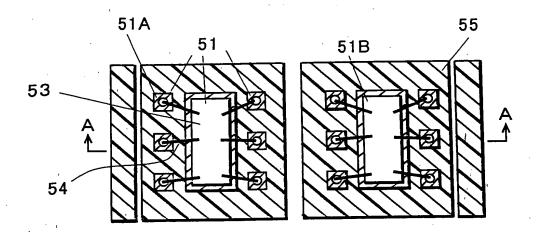


FIG.17B

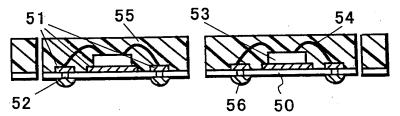


FIG.17C

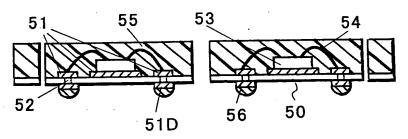


FIG.18

